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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Cohen et al. Examiner: Dang T. Nguyen  
Serial No.: 10/695,449 Group Art Unit: 2824  
Filed: 10/29/2003  
Title: METHOD SYSTEM AND CIRCUIT FOR PROGRAMING A NON-VOLATILE MEMORY ARRAY

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**RESPONSE**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This correspondence is filed in response to the Office Action dated March 9<sup>th</sup> 2005 issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the Office Action is due June 9, 2005. Accordingly, this Amendment is being timely filed.

**Remarks/Arguments begin on page 2 of this paper.**